IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Group Art Unit:**

Intellectual Property Law Department Aguilar, et. al.

Serial No.: **International Business Machines Corporation** Filed:

Title: System and Method for Managing a : 11400 Burnet Road

Plurality of Processors as Devices : Austin, Texas 78758

INFORMATION DISCLOSURE STATEMENT

The Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

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Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56 (a) exists. The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR § 1.98 (a)(1). A copy of each of the non-U.S. Patent documents on PTO-1449 is supplied herewith.

This application is being filed on the same day as the following U.S. Patent Applications, each having the same assignee (International Business Machines Corporation):

"System and Method for Loading Software on a Plurality of Processors," by Aguilar, Chow, Day, Gowen, Nutter, and Xenidis (Docket No. AUS920030717US1);

"System and Method for Processor Thread Acting as a System Service Processor," by Aguilar, Nutter, and Stafford (Docket No. AUS920030700US1);

"System and Method for Processor Thread for Software Debugging," by Aguilar, Day, Manning, and Nutter (Docket No. AUS920030698US1);

"System and Method for Processor Dedicated Code Handling in a Multi-Processor Environment," by Aguilar, Nutter, and Stafford (Docket No. AUS920030791US1);

"System and Method for Asymmetric Heterogeneous Multi-Threaded Operating System," by Aguilar, Day, Nutter, and Stafford (Docket No. AUS920030699US1);

"System and Method for Grouping Processors," by Aguilar, Day, Nutter, and Xenidis (Docket No. AUS920030706US1);

"System and Method for Virtual Devices Using a Plurality of Processors," by Aguilar, Day, Nutter, and Xenidis (Docket No. AUS920030705US1);

"System and Method for Encrypting Data Using a Plurality of Processors," by Aguilar, Craft, Day, Hofstee, and Nutter (Docket No. AUS920030704US1);

"System and Method for Manipulating Data with a Plurality of Processors," by Day, Nutter, and To (Docket No. AUS920030718US1);

"System and Method for Solving a Large System of Dense Linear Equations," by Nutter and To (Docket No. AUS920030703US1);

"System and Method for Compiling Source Code for Multi-Processor Environments," by Brokenshire, Minor, Nutter, and To (Docket No. AUS920030701US1);

"System and Method for Balancing Computational Load Across a Plurality of Processors," by Minor, Nutter, and To (Docket No. AUS920030790US1);

"System and Method for Dynamically Partitioning Processing Across Plurality of Heterogeneous Processors," by Brokenshire, Minor, and Nutter (Docket No. AUS920030702US1); and

"System and Method for Task Queue Management of Virtual Devices Using a Plurality of Processors," by Aguilar, Day, Minor, Nutter, and Xenidis (Docket No. AUS920030844US1).

While these co-pending applications may be "material" pursuant to 37 CFR § 1.56, it has not been ascertained that the aforementioned applications are "related" to the present application.

Respectfully submitted,

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In Place of FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT				Serial No. Applicant: Aguilar, et. al. Filing Date: Group: Atty. Docket No. AUS920030707US1				
U. S. PATENT DOCUMENTS								
Examiner <u>Initial</u> ———————————————————————————————————	AA AB AC AD AE AF AG AH AI	Document Number 2003/0055984 2002/0156993 2002/0138707 2002/0138637 2002/0135582 2002/0095523 2002/0060690 2002/0046229	Date Jul. 18, 2002 Oct. 24, 2002 Sep 26, 2002 Sep 26, 2002 Sep 26, 2002 Sep 26, 2002 Jul. 18, 2002 May 23, 2002 Apr. 18, 2002	Name Shimakawa et al. Suzuoki et al. Tanaka et al. Yutaka et al.	Class 709 712 711 711 709 345 709 345 709	Subclass 316 30 163 154 230 502 316 619 102	Filing Date if Appropriate Oct. 12, 2001 Mar 22, 2001 Oct. 12, 2001 Mar. 1, 2001 Mar. 2, 2001	
	AJ AK AL	6,559,854 2001/0002130 6,526,491	May 6, 2003 May 31, 2001 Feb. 25, 2003	Oka et al. Suzuoki Suzuoki et al.	345 345 711	619 420 164	Mar. 1, 2001 Dec 15, 2000 Mar 22, 2001	
FOREIGN PATENT DOCUMENTS								
Examiner Initial	AM AN AO AP	Document <u>Number</u>	<u>Date</u>	Country	Class	Subclass	Translation <u>Yes No</u>	
Examiner Initial	AQ AR AS AT	<u>o</u> 1	THER ART (Inclu	ding Author, Title, Da	ate, Perting	ent Pages, Etc	c.)	
Examiner:	AU							
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								